

Latency Hiding in Tree Lookups using Out Of Order Execution

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November 28, 2017

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Adaptive Rradix Tree



Adaptive Rradix Tree

Whats so special?



$A_{\text{daptive}} R_{\text{radix}} T_{\text{ree}}$

Whats so special?

Improved radix tree (or prefix tree)



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Whats so special?

- Improved radix tree (or prefix tree)
- Dynamically adjusts node size



Adaptive Rradix Tree

Whats so special?

- Improved radix tree (or prefix tree)
- Dynamically adjusts node size
- Can compress paths

Example radix tree



Different node types



- Node4
- Node16
- Node48
- Node256

Different node types

- ► Node4
- Node16
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Example Node4:

Keys (1B each)				Pointer (8B each)					
0	13	42	255	Ptr to 0	Ptr to 13	Ptr to 42	Ptr to 255		

Different node types

- ► Node4
- Node16
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Example Node4:

Keys (1B each)				Pointer (8B each)					
0	13	42	255	Ptr to 0	Ptr to 13	Ptr to 42	Ptr to 255		

Lookup using findChild()

Lookup algorithm



Lookup algorithm



```
lookup(node, key, depth):
    if node == NULL
2
      return NULL
3
    if isLeaf(node)
4
      if leafMatches(node, key, depth)
5
         return node
6
      return NULL
7
    // ...
8
    next = findChild(node, key[depth])
9
    return lookup(next, key, depth+1)
10
```

Lookup algorithm



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What is Out Of Order Execution?



(a+b)+(c+d)

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No dependency between (a+b) and (c+d) \rightarrow Can be calculated in parallel

(a+b)+(c+d)

No dependency between (a+b) and (c+d) \rightarrow Can be calculated in parallel

Especially helpful for expensive operations, like memory accesses

ТЛП

One list

Linked List data type:

```
1 struct Node {
2 Node *next;
3 std::uint8_t data[56];
4 };
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1 for (Node *curr = list;
2 curr != nullptr;
3 curr = curr->next) {
4 // Empty body
5 }
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One list

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1 struct Node {
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Iteration:

```
1 for (Node *curr = list;
2 curr != nullptr;
3 curr = curr->next) {
4 // Empty body
5 }
```

In Assembler:

1	0×3590:	mov	(%rax),%rax						
2	0×3593:	test	%rax,%rax	;	depends	on	the	first	instr.
3	0×3596:	jne	0×3590						

Two lists



Two lists

```
1 for (Node *curr1 = list1, *curr2 = list2;
2 curr1 != nullptr && curr2 != nullptr;
3 curr1 = curr1->next, curr2 = curr2->next) {
4 // Empty body
5 }
```

Two lists

```
1 for (Node *curr1 = list1, *curr2 = list2;
2 curr1 != nullptr && curr2 != nullptr;
3 curr1 = curr1->next, curr2 = curr2->next) {
4 // Empty body
5 }
```

In Assembler:

1	0×3600:	mov	(%rax),%rax			
2	0×3603:	mov	(%rdx),%rdx	;	No	dependency !
3	0×3606:	test	%rax,%rax			
4	0×3609:	je	0×3610			
5	0×360b:	test	%rdx,%rdx			
6	0×360e:	jne	0×3600			
7	0×3610:					
1						

Linked List Experiment Results



ТШ

Linked List Experiment Results



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Basic idea





Perform multiple lookups at the same time

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This technique is called Group Prefetching

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Keep track of every lookup

Tracking each state



How can we track the state of each lookup?

Tracking each state

How can we track the state of each lookup?

```
struct GPState {
1
    std::uint8 t key[8];
2
    Node *node;
3
4
    unsigned depth = 0;
5
    // ...
6
    bool finished = false;
7
8
    GPState() : node(nullptr) {}
9
    GPState(Node *node) : node(node) {}
10
  };
11
```

The actual lookup algorithm



The actual lookup algorithm

```
void lookupGP(std::vector<GPState> &states) {
1
    while (/* not all finished */) {
2
      // Loop over every state
3
      for (auto &state : states) {
4
         if (state.finished)
5
           continue:
6
7
         // Perform the normal lookup algorithm step
8
         if (state.node == NULL || isLeaf(state.node)) {
9
           state.finished = true:
10
           continue;
11
12
         state.node = *findChild(state.node,
13
                                   state.key[state.depth]);
14
         state.depth++;
15
      }
16
17
18
```







► TPC-H benchmark (see e.g. HyperDB Webinterface)





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- Joining lineitem with orders



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- Iterating the tuples in lineitem and performing a lookup in the ART for orders (with multiple keys using GP)



- ► TPC-H benchmark (see e.g. HyperDB Webinterface)
- Joining lineitem with orders
- lineitem has foreign key to orders
- Creating an index on orders
- Iterating the tuples in lineitem and performing a lookup in the ART for orders (with multiple keys using GP)
- Amount of parallel lookups is called Group Size

Benchmarking Results Ordered



Benchmarking Results Ordered





Benchmarking Results



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Benchmarking Results



$My \ reaction$



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Group Prefetching ...

 ... increases Performance, but not as much as seen in the Linked List experiment

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 - \rightarrow perfect value depends on use case and hardware

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Out Of Order Execution is quite cool

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Q & A

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